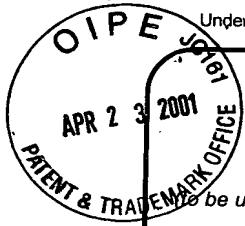


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PTO/SB/21 (08-00)

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Application Number	08/988,246
Filing Date	December 1, 1997
First Named Inventor	Sebastien Raoux
Group Art Unit	1763
Examiner Name	Rudy Zervigon
Attorney Docket Number	A17714T19930

ENCLOSURES (check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (for an Application)	<input type="checkbox"/> After Allowance Communication to Group
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<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):
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<input type="checkbox"/> Response to Missing Parts/ Incomplete Application		
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

Remarks The Commissioner is authorized to charge any additional fees to Deposit Account 20-1430.

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm and Individual name	Townsend and Townsend and Crew LLP Chun-Pok Leung	Reg No. 41,405
Signature		
Date	4-17-01	

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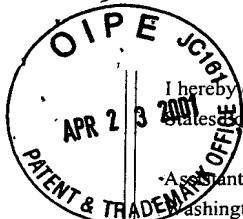
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PATENT
Attorney Docket No.: A1771-4T19930
TTC No.: 16301-019930

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

SEBASTIEN RAOUX et al.

Application No.: 08/988,246

Filed: December 1, 1997

For: METHOD AND APPARATUS FOR
MONITORING AND ADJUSTING
CHAMBER IMPEDANCE

Examiner: Rudy Zervigon

Art Unit: 1763

AMENDMENT

#23F
4625101
MW

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action mailed January 17, 2001, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claims 11 and 20; cancel claims 17, 18, and 25 without prejudice, and add new claims 27-30.

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1. PREVIOUSLY CANCELED.
2. PREVIOUSLY CANCELED.
3. The substrate processing system of claim 11 further comprising a computer processor communicatively coupled to said impedance monitor so that said computer processor receives as an input the measured impedance level of said plasma.
4. The substrate processing system of claim 3 further comprising a variable capacitor electrically coupled to said chamber and controllably coupled to said processor wherein